

Electronic Acknowledgement Receipt

EFS ID:	8248406
Application Number:	10587725
International Application Number:	
Confirmation Number:	2259
Title of Invention:	Method for Manufacturing Soi Wafer
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Filer Authorized By:	Anton E. Skaugset
Attorney Docket Number:	SHX 373
Receipt Date:	18-AUG-2010
Filing Date:	26-JUL-2006
Time Stamp:	22:01:16
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	20100818_Third_Supplemental _IDS_SB08a-EFS_as- filed_SHX373.pdf	612512 07c25299a38295db3a449160f122621075-428a	no	4

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2	Foreign Reference	JP0411551_Suzuki_w-English-abs.pdf	888638 c094c4f6e70b96796c381550bae73c25930894b0a	no	5
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3	Foreign Reference	JP04206766_Shigeniwa_w-English-abs.pdf	1326539 5c8d7ae1b0f77c707ba09e298028c0bc0e9203776cd	no	7
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4	Foreign Reference	WO93-20584_Gassel_etal_w-English-abs.pdf	4183441 10b0c10f235f7a01bfba0a05980261c0cb0c741c	no	20
Warnings:					
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5	Foreign Reference	JP07226433_Hashimoto_w-English-abs.pdf	3441981 81cd99289ed40c042466cd557424de9f82a00d4	no	13
Warnings:					
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6	NPL Documents	GASSEL_etal_SIMOX_and_Wafer_Bonding.pdf	1889710 5ea2914b0a0c13934b0f454d536ae0882b73940	no	2
Warnings:					
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7	NPL Documents	TONG_etal_VLSI_SOI_Fabrication_by_SIMOX_Wafer_Bonding.pdf	718360 0b0c3980d24054801a0fa090145461deb722af	no	2
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8	NPL Documents	YANG_etal_Silicon-on-Insulator-with-Active-Substrate.pdf	730908 1127ec2369b3aed0f6cd950b0c0c0e0a09520a0	no	2
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